



SRI VASAVI INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE, New Delhi & Permanently Affiliated to JNTU Kakinada)

Accredited by NAAC & NBA (CSE, ECE & ME), (An ISO 9001:2015 Certified Institute)

Nandamuru, Pedana Mandal, Krishna Dist – 521 369

Department of Electronics and Communication Engineering

A.Y :2023-24

Mechanism of Internal assessment

The internal marks are based on the Mid exams (descriptive examinations, online quiz exam) and Assignments. Two mid examinations are conducted. The answer scripts of examinations are shown to the students after evaluation to bring out the discrepancies, if any, to the notice of teacher concerned, and the necessary corrections are carried out. Faculty explains about scheme of evaluation of each course in the class room. The model answer key is also available in the faculty course file for further verification by the students (if required). Assignment marks are allotted based on time of submission before deadline and presentation methods utilized. After correction, the consolidated marks are displayed in the notice boards. Internal marks are intimated to the parents over phone by the counsellor of the student. The students are made aware of the evaluation procedures including revaluation and challenge valuation, examination pattern of college and the university, well in advance through circulars.

A Sample Scheme of Evaluation for VLSID Internal Question Paper is shown below:

SCHEME OF EVALUATION:

III B.Tech. II SEM II Mid Examinations

Subject: VLSID (C323)

Branch: ECE (A & B)

Time: 2:45 PM to 04:15PM

Max.Marks:30M **Date:**30-04-2024.

SCHEME OF EVALUATION

3X 5 = 15 Marks

1. Expressions for input impedance, voltage gain and output impedance of common source amplifier with source resistor and About body bias effect (5 M)
2. (i) Static power dissipation(1M)
(ii) Dynamic power dissipation(1M)
iii) Master-Slave based edge triggered register with a neat diagram (3M) (5 M)

3.(i)FPGA design flow.(1M)

(ii) Fin-FET Technology (2M)

(iii) TFET technology(2M)

(5 M)

Total

30M


Principal
Sri Vasavi Institute of
Engineering & Technology
NANDAMURU


Coordinator