

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
SRI VASAVI INSTITUTE OF ENGINEERING & TECHNOLOGY

Accredited by NBA (CSE, ECE & MECH) & NAAC with 'A' Grade

(Approved by AICTE, New Delhi, Permanently Affiliated to JNTU Kakinada, Kakinada)

Nandamuru, Pedana Mandal-521369, Krishna Dt., Andhra Pradesh



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CERTIFICATE

This is to certify that the thesis entitled "DESIGN AND IMPLEMENTATION OF HYBRID FSO/RF SYSTEM" being submitted by

L. JAYA NAVYA SRI

20MQ1A0465

A.RESHMA

20MQ1A0456

A.KAMAL KHAN

20MQ1A0476

G.ISHWARYA

20MQ1A0459

In partial fulfillment of the requirements for the award of degree of B.Tech in Electronics and Communication Engineering from **Jawaharlal Nehru Technological University Kakinada, Kakinada** is a record of bonafide work carried out by them at Sri Vasavi Institute of Engineering & Technology

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Associate Professor

HOD

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Pedana Mandal-521369, Krishna Dt., Andhra Pradesh



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CERTIFICATE

This is to certify that the thesis entitled "**VOICE CONTROLLED HOME AUTOMATION
SYSYTEM**" being submitted by

B.TIRUMALASRI DEVI

21MQ5A0410

E.KUSUMA SAI

20M Q1A0458

K.ADARSH TEJA

20MQ1A0477

P.PAVAN KALYAN

20MQ1A0496

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Project Guide

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Department of Electronics and Communication Engineering



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CERTIFICATE

This is to certify that the thesis entitled "**Cascode Cross-Coupled Stage High-Speed Dynamic Comparator in 65nm CMOS**" is being submitted by

K.LIKHITHA	21MQ5A0411
T.N.S.NARASIMHA RAO	20MQ1A04A0
V.CHANDRIKA	20MQ1A0474
S.SUDEEP	20MQ1A0497

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Nandamuru, Pedana Mandal-521369, Krishna Dt., Andhra Pradesh

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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CERTIFICATE

This is to certify that the thesis entitled "**DESIGN AND IMPLEMENTATION OF AREA-OPTIMIZED AES ALGORITHM USING PIPELINE TECHNOLOGY**" being submitted by


N. VENKATA NAGA RAMYA	20MQ1A0467
A. POORNIMA	20MQ1A0454
V.VEERA VINODH BABU	20MQ1A04A3
K. GANESH VAMSI	20MQ1A0490

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Project Guide 18/5/24
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ISO 9001:certified institute



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CERTIFICATE

This is to certify that the thesis entitled "IOT BASED AUTOMATIC SHED SYSTEM TO PREVENT UNWANTED RAIN SENSOR FOR GROWING CROPS (mostly used tobacco,chilli plants)" being submitted by

T.CHINNA KALYANI

20MQ1A0470

M.LIDHIN KUMAR

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V.BHARGAVI

20MQ1A0473

G.SOMENDRA PAVAN

20MQ1A0486

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Project Guide

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CERTIFICATE

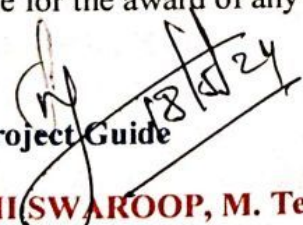
This is to certify that the thesis entitled “**DESIGN AN HIGH EFFICIENT ERROR DETECTION AND CORRECTION FOR MEMORIES**” being submitted by

R. KALPANA
K. PRERITHA
P. PRUDHVI RAJ
K. TAGORE NATH

21MQ5A0413
20MQ1A0463
20MQ1A0494
20MQ1A0499

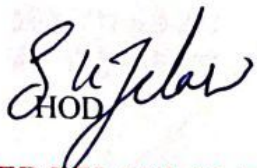
In partial fulfillment of the requirements for the award of the degree of **B. Tech** in Electronics and Communication Engineering from **Jawaharlal Nehru Technological University Kakinada**, a record of bonafide work carried out by them at **Sri Vasavi Institute of Engineering & Technology**

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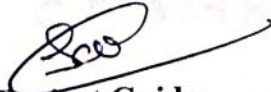
CERTIFICATE

This is to certify that the thesis entitled "A Reliable and High Speed 6T Compute SRAM Design with Dual-Split-V_{DD} Assist and Bitline Leakage Compensation" being submitted by

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Nandamuru, Pedana Mandal, Krishna Dt, A.P. 2020 – 2024.



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CERTIFICATE

This is to certify that the thesis entitled “ **Automatic agriculture robot**” is being submitted by

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G.MAHESH

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K.BHANU PRAKASH

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The results embodied in this project report have not been submitted to any other University or Institute for the award of any degree or diploma.


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(An ISO 9001:2015 CERTIFIED INSTITUTE)

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CERTIFICATE

This is to certify that the thesis entitled “DESIGN OF HIGH EFFICIENT AND HIGH SPEED PARALLEL PREFIX MULTIPLIER” being submitted by

R.SOWMYA SREE

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I. SRI SAI SAJANA

20MQ1A0460

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CH.HARI NAGA BABU

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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CERTIFICATE

This is to certify that the thesis entitled “**Design and Optimization of an Energy-Efficient Transmission Gate Level Shifter Circuit**” being submitted by

V.KUSUMA

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CH.PHANINDRA MANOJ

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An ISO 9001:2015 Certified Institute



CERTIFICATE

This is to certify that the thesis entitled **“Implementation of a Multi Clocked Pipelined Processor Based on RISC-V using RV32I”** being submitted by

S. SANDHYA RANI

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B. SATHISH

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T. MADHU

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Nandamuru, Pedana Mandal-521369, Krishna Dt., Andhra Pradesh



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CERTIFICATE

This is to certify that the thesis entitled "DESIGN AND IMPLEMENTATION OF
LOW POWER BAUGH-WOOLEY MULTIPLIER USING REVERSIBLE
CIRCUITS" being submitted by

A. HEMA NAGA SAI VIJAYA LAKSHMI **20MQ1A0453**

K. SUCHARITA **20MQ1A0461**

J. RAGHUVVEER **20MQ1A0487**

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Nandamuru, Pedana Mandal-521369, Krishna Dt., Andhra Pradesh

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the thesis entitled “**Design and Implementation of AES S-Box using RFF**” being submitted by

T. SRAVYA

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CH. N KV PRASAD

20MQ1A0482

N. SUDHEER KUMAR

20MQ1A0493


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Department of Electronics and Communication Engineering



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CERTIFICATE

This is to certify that the thesis entitled "**Adiabatic Positive Feedback Method for Improving Energy Efficiency of Single-Ended 10T Near-Threshold SRAM Design**" being submitted by

N.KIRANMAYI

20MQ1A0466

K.GANESH KUMAR

20MQ1A0488

E.SASHANK

20MQ1A0484

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Nandamuru, Pedana Mandal - 521369, Krishna Dt., Andhra Pradesh.



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CERTIFICATE

This is to certify that the thesis entitled "LI-FI OR VLC BASED INDOOR LOCALIZATION USING MACHINE LEARNING" being submitted by

GULE NIKHAT

(20MQ1A0408)

K. RAJESH

(20MQ1A0439)

K. CHINMAYA

(20MQ1A0412)

T. TEJASWI

(20MQ1A0423)

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Pedana Mandal-521369, Krishna Dt., Andhra Pradesh



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CERTIFICATE

This is to certify that the thesis entitled "**FINGERPRINT BASED ELECTRONIC-VOTING MACHINE**" being submitted by

V.S.L.S.R. DURGA LAKSHMI

20MQ1A0424

S.D.S.N.S. MANIKANTA

20MQ1A0449

P.V. SIVA DURGA

20MQ1A0419

G. GANYA SAI

20MQ1A0434

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SRI VASAVI

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Accredited by NAAC & NBA (CSE, ECE & ME)
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An ISO 9001:2015 Certified Institute



CERTIFICATE

This is to certify that the thesis entitled "**Low-Power Redundant-Transition-Free Tspc Dual-Edge-Triggering Flip-Flop Using Single-Transistor-Clocked Buffer**" being submitted by

P. NIKHITHA

21MQ5A0404

G. SAI MANIKANTA

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B.K. KUMUDA

20MQ1A0403

M. GEETHIKA

20MQ1A0416

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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CERTIFICATE

This is to certify that the thesis entitled “**DESIGN AND IMPLEMENTATION OF 32-BIT MAC UNIT USING VEDIC MATHEMATICS**” being submitted by

**B. DEEPTHI
CH. SAI HARINI NAG
K. BALA KRISHNA
P. BHARGAV
V. YUGANDHAR**

**21MQ5A0401
20MQ1A0405
20MQ1A0440
20MQ1A0445
20MQ1A0451**

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CERTIFICATE

This is to certify that the thesis entitled "**IOT BASED HEALTH TRACKING SHOE FOR ELDERLY PEOPLE BY USING THE GAIT MONITORING SYSTEM**" being submitted by

D.Vineela	20MQ1A0406
N.Vijaya Lakshmi	20MQ1A0417
Ch.Viraj	20MQ1A0430
P.N.S.Satyanarayana	20MQ1A0444

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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CERTIFICATE

This is to certify that the thesis entitled "**A HIGH-SPEED MONTGOMERY MULTIPLIER**"
Being submitted by

M.DEEVENA KUMARI	20MQ1A0415
Y. NAVYA SREE	21MQ5A0405
B. GOWTHAMI	20MQ1A0404
K. LAKSHMI HARIKA	20MQ1A0411

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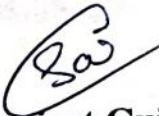
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This is to certify that the thesis entitled "**LPG GAS LEAKAGE DETECTION AND AVOIDANCE SYSTEM**" being submitted by

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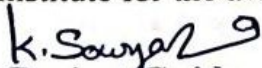
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This is to certify that the thesis entitled "IOT BASED SMART ACCIDENT
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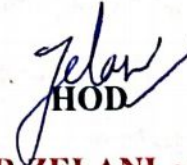
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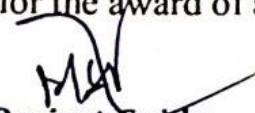
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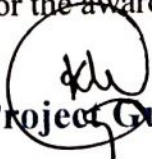
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This is to certify that the thesis entitled “**DESIGN AND IMPLEMENTATION OF LOW POWER ARITHMETIC AND CODE CONVERTING CIRCUITS USING REVERSIBLE LOGIC**” being submitted by

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This is to certify that the thesis entitled "**DESIGN LOW POWER PERFORMANCE PROPOSED FULL-ADDER ARCHITECTURE**" being submitted by

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20MQ1A0422
20MQ1A0447
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