

## **FACULTY PROFILE**

1. Name : Dr. Bonam Raghavaiah
2. Date of Birth : 16-05-1981
3. Educational Qualifications : B.E, M.Tech, PhD
4. Work Experience (in years) : 14yr
  - a) Teaching : 14 yr
  - b) Research : 3 years
  - c) Industry :
  - d) Others :
5. Area of Specialization : VLSI design, Hybrid Parallel Pre-fix adders.
6. Subjects taught at
  - a) Under Graduate Level : EDC, ECA, MP&I, TVE, CMC, DSP&A, VLSI, DC, STLD, TC&SS, EMI, BEE
  - b) Post Graduate Level : DSP&A, HS&D, LPVLSI
7. No. of Research Publications
  - a) National Journals :
  - b) International Journals : 23
  - c) International Conference : 5
  - d) National Conference : 2
8. Short term courses attended :
9. Membership of Professional Societies/Academic Bodies : ISTE



Signature of the Faculty