

SRI VASAVI INSTITUTE OF ENGINEERING AND TECHNOLOGY DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COUSE OUTCOMES SUMMARY III-I ECE A.Y:2020-21

Empowering	COUSE OUTCOMES SUMMARY III-I ECE A.Y:2020-21			
CO#	CO STATEMENT	BTL		
	Computer Architecture and			
	Organization (C311)			
C311.1	Understand the architecture of ancient and modern computers, distinguish software &	Analyzing		
	Hardware and Analyze the performance of a computer using performance equation	Understanding		
C311.2	Familiar about instruction, addressing mode and different languages			
C311.3	Identify different types of instructions and addressing modes.			
C311.4	Differentiate I/O devices and interface circuits.			
C311.5	Analyze the performance of the hierarchical of memory	Analyzing		
C311.6	Differentiate the Hardwired and Micro Programmed control	Analyzing		
	Linear I C Applications (C312)			
C312.1	Identify different configurations of op-amp analyze the parameters of op-amp and			
	observe the frequency response of operational amplifier	Analyze		
C312.2	Understand non ideal characteristics of operational amplifier parameters	Understand		
C312.3	Demonstrate linear and non applications of operational amplifiers	Apply		
C312.4	Select active filter, multipliers and modulators according to the required application	Apply		
C312.5	Implement various applications of special function Op-Amp ICs such as 555 IC and			
	Analog multiplier, PLL.	Analyze		
C312.6	Demonstrate and compare the performance of various types of ADC and DAC using Op-	Tillaryze		
C312.0	Amp	Apply		
	Digital I C Applications (C313)	Арріу		
C313.1	Discuss the basic concepts of different logic families and how to interface different			
C313.1	CMOS logic families.	Understand		
C313.2	Describe about the basic elements of VHDL Programming language.	Understand		
C313.3	7 7 7			
C313.4	Illustrate different modeling techniques in digital electronics.			
C313.4	Analyze, design, simulate and implement combinational logic circuits using VHDL. Analyze, design, simulate and implement sequential logic circuits using VHDL.			
C313.3		Create		
C313.6	Evaluate and Create different models of Finite State Machines for design of sequential			
	circuits.	Create		
	Digital Communications(C314)			
C314.1	Analyze the recording of the digital data from a analog data in a compact disc	Analyze		
C314.2	Analyze & Apply various types of digital communication methods. Eg: Mobile	Analyze		
	Communications and Computer Network	1 111111 / 20		
C314.3	Analyze & Calculate BER in digital communication channels	Analyze		
C314.4	Analyze the information rate, entropy & channel capacity in the information transmission	Analyze		
C317.7	channel.	, 20		
C314.5	Analyze suitable source code for the given application	Analyze		
C314.6	Analyze & Design different error correcting codes for the given application.	Analyze		
	Antenna and Wave Propagation(C315)	<i>y</i>		
C315.1	Understand the concepts of Antenna fundamentals and its radiation.	Understand		
C315.2	Analiyze radiation mechanism for different antennas	Analyze		
C315.3	Design and analyze the antenna arrays			
C315.4	Analyze the reflector antennas, lens Antennas, horn antennas and micro strip antenna			
C315.5	Discuss the techniques to measure the Gain, Radiation pattern of an Antenna	Analyze Understand		
C315.6	Explain the mechanism of the atmospheric effects on radio wave propagation.			
	2	Understand		