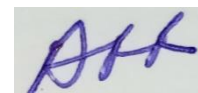


FACULTY PROFILE

1. Name : Dr. ADDANKI PURNA RAMESH
2. Date of Birth : 11-06-1973
3. Educational Qualifications: M.Tech., Ph.D.
4. Work Experience (in years):
 - a) Teaching : 22
 - b) Research : 13
 - c) Industry : 03
 - d) Others : --
5. Area of Specialization : VLSI
6. Subjects taught at
 - a) Under Graduate Level : STLD, PDC, CAO, DICA, VLSI Design, ES, DIP, DDV, SDD, DLD.
 - b) Post Graduate Level : DSD, CPLD&FPGA, VHDL, AVLSIDA
7. No. of Research Publications
 - a) National Journals : NIL
 - b) International Journals : 35
 - c) International Conference : 3
 - d) National Conference : NIL
8. Short term courses attended : 19
9. Membership of Professional Societies/Academic Bodies : AMIE, IETE, ACEEE, IEEE



Signature of the Faculty